

# Mealy Circuits vs. Moore Circuits

Comp 260 Fall Semester 2002

October 22, 2002

| <i>Mealy<br/>Circuits</i>   | <i>Moore<br/>Circuits</i>   |
|---|---|
| <p>(1.) Current outputs are affected by the <i>current state and the current inputs</i></p> <p>(2.) Outputs are <i>unstable</i> until current inputs achieve steady state</p> <p>(3.) <i>More difficult</i> to engineer because of the unstable outputs</p> <p>(4.) Require <i>less</i> hardware than Moore circuits</p> <p>(5.) Inputs can effect outputs in <i>current clock period</i></p> | <p>(1.) Current outputs are affected by the <i>current state only</i></p> <p>(2.) Current outputs are <i>always stable</i> since they depend only on the current state which is always stable</p> <p>(3.) <i>Easy</i> to engineer since current outputs are always stable</p> <p>(4.) Require <i>more</i> hardware than Mealy circuits</p> <p>(5.) Inputs can affect outputs in <i>next clock period only</i></p> |